

C1

determining a maximum frequency which provides  
power not greater than the allowable power consumption level,  
determining a minimum voltage which allows operation at the maximum  
frequency determined, and  
dynamically changing the operating condition of the processor by  
changing one of the frequencies generated by the clock generator and the voltage  
to the maximum frequency and the minimum voltage determined, wherein a  
plurality of clock frequencies are provided which can be individually selected  
concurrently.

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